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PATENT
Attorney Docket No. 401191

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

HATAKENAKA et al.

Application No. 09/871,978

Art Unit: 2133

Examiner: D. Ton

Filed: June 4, 2001

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE COMPRISING
RAM WITH COMMAND DECODE
SYSTEM AND LOGIC CIRCUIT
INTEGRATED INTO A SINGLE
CHIP AND TESTING METHOD OF
THE RAM WITH COMMAND
DECODE SYSTEM

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Technology Center 2100

RESPONSE TO OFFICE ACTION

U.S. Patent and Trademark Office
220 20th Street S. Customer Window, Mail Stop Amendment
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Dear Sir:

In response to the Office Action dated March 30, 2004, please enter the following amendments and consider the following remarks.